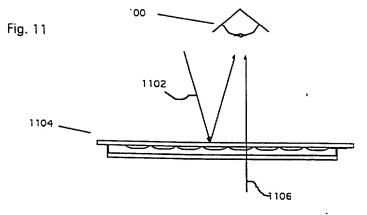


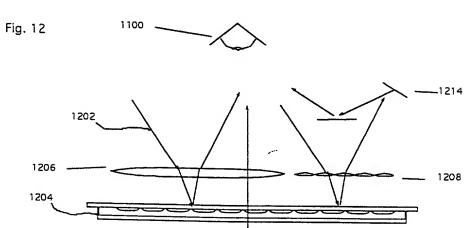
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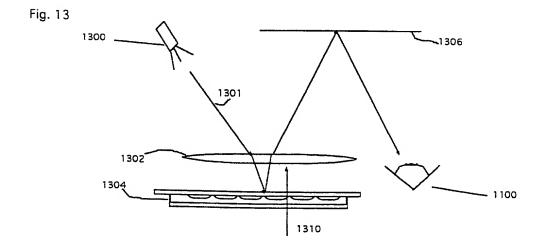
Fig. 10A

n bit Shift

Register

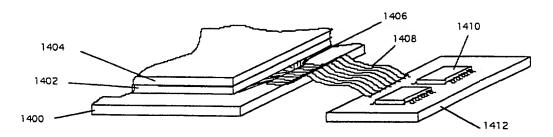


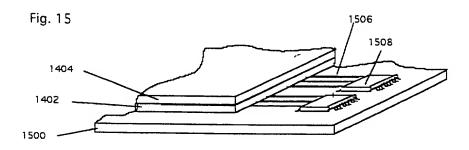


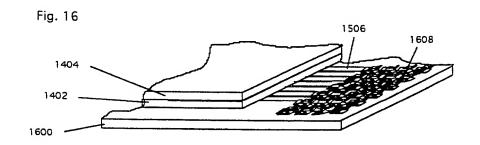


-1212

Fig. 14







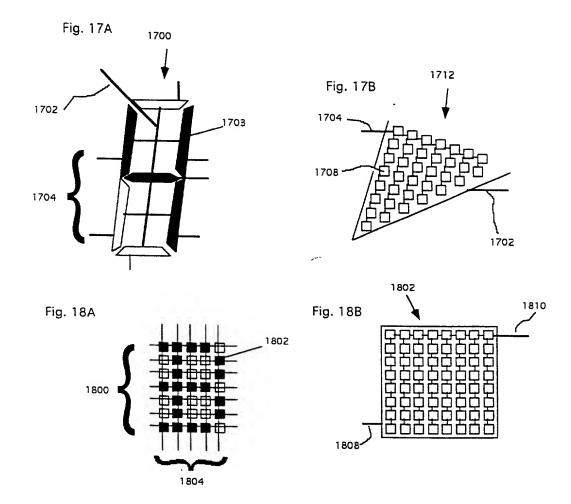
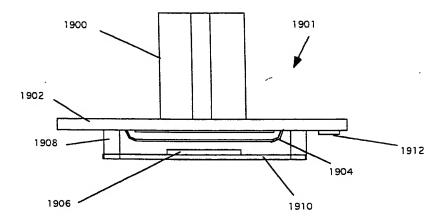
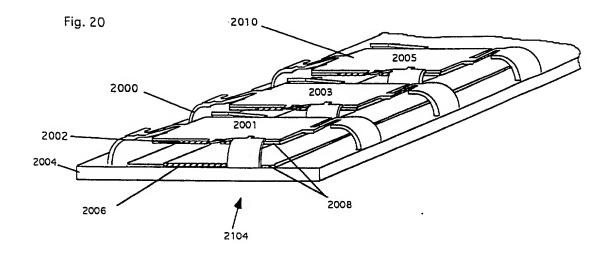
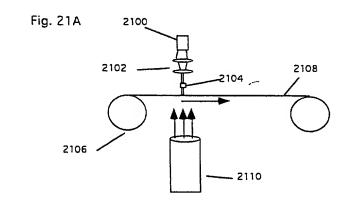
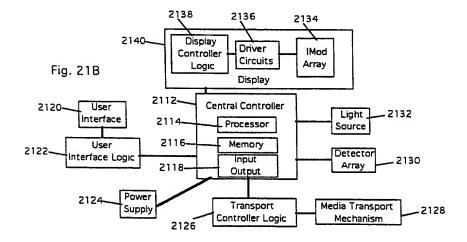


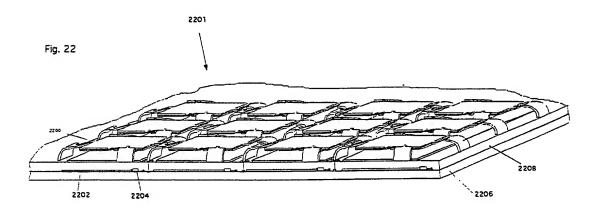
Fig. 19

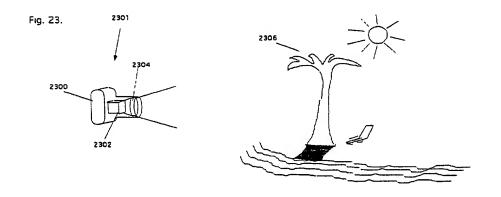


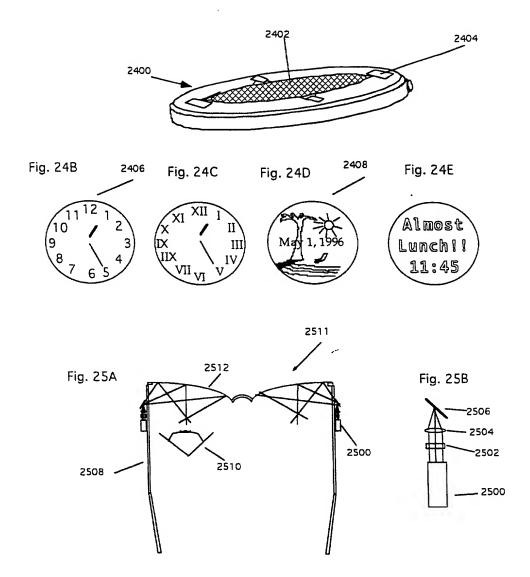


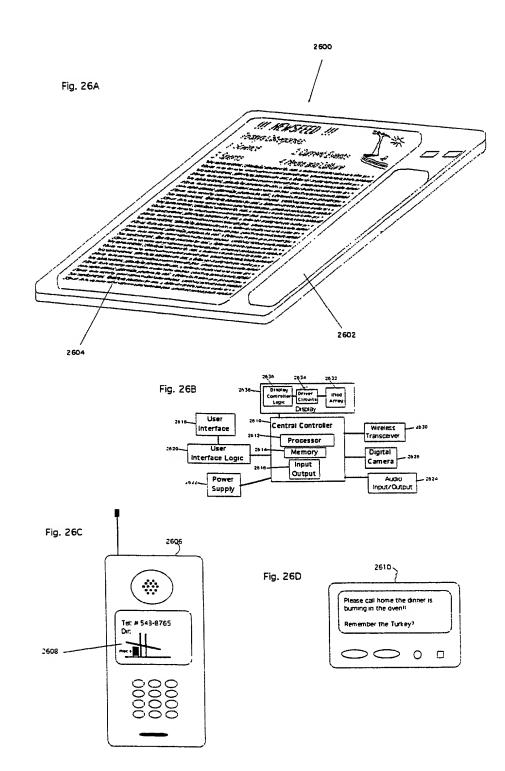


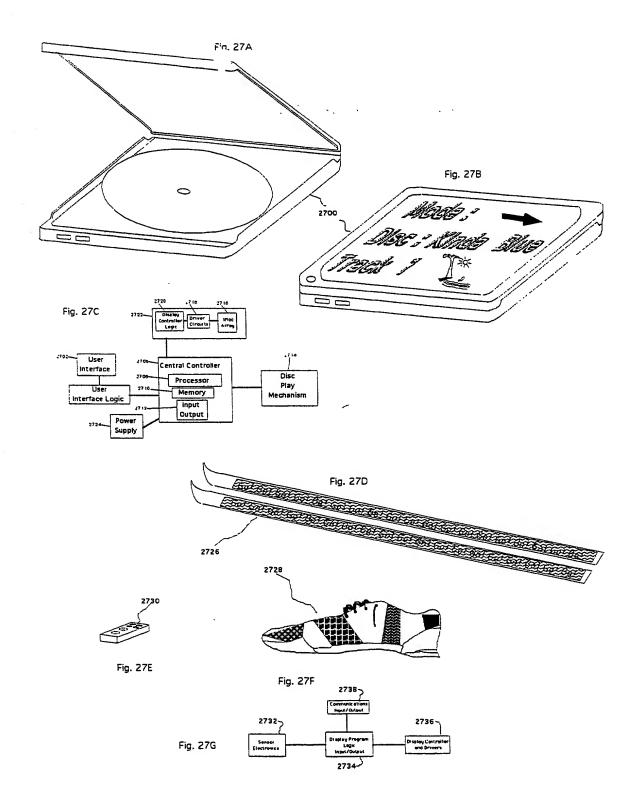


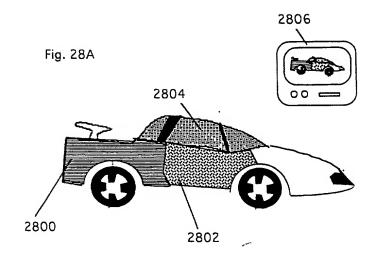


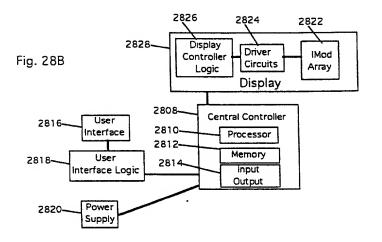












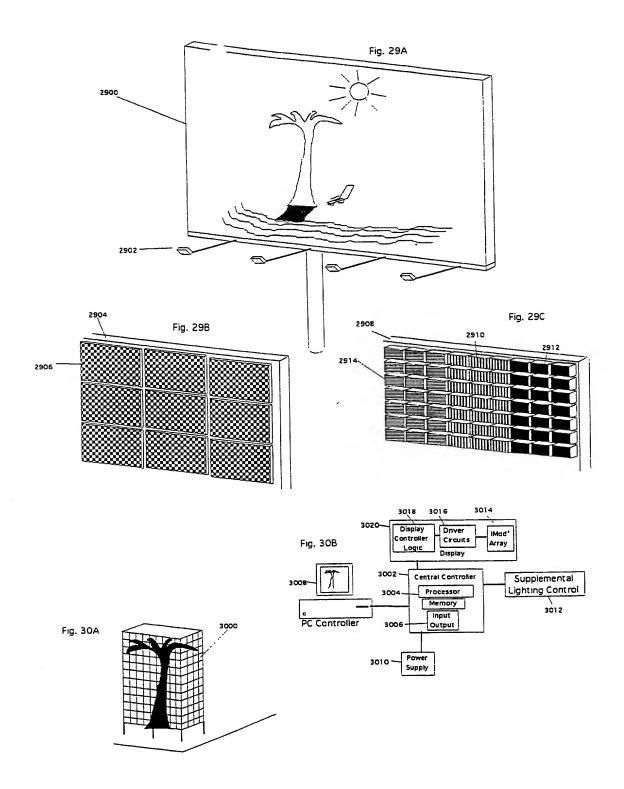


Fig. 31A

